



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yojiro MATSUEDA et al.

Group Art Unit: 2815

Application No.: 10/676,072

Examiner: P. BUDD

Filed: October 2, 2003

Docket No.: 117391

For: CIRCUIT SUBSTRATE, MANUFACTURING METHOD THEREOF, ELECTRO-  
OPTICAL DEVICE, AND ELECTRONIC APPARATUS

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the February 28, 2007 Office Action, please consider the following:

**Amendments to the Claims** as reflected in the listing of claims; and

**Remarks.**

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02 FC:1201

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